

Electronic Acknowledgement Receipt

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Confirmation Number:	3423
Title of Invention:	System and method for suppression of wafer temperature drift in cold-wall cvd systems
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1		US020527US_RestrictionRe spTraverse_2008-AUG-05.p df	117695 e22f57996c2d6a0d729f169fd82b48428 0674764	yes	3
	Multipart Description/PDF files in .zip description				
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	Response to Election / Restriction Filed		1	1	
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2	Information Disclosure Statement (IDS) Filed (SB/08)	US_020527US_IDS_Form_ SB_08a_2008-AUG-05.pdf	573431 de89ac48bf1dd92156f84f6bf5c012d49 b31c843	no	4
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3	NPL Documents	US020527WO1_Invitationto PayFees_2004-JUN-14.pdf	214554 d3070c2ae0a311f4bc806931f046fbc70 272a827	no	4
Warnings:					
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4	Foreign Reference	WO2002065521A1.pdf	1778894 749877c99e2b9b57a55b1674276f8c41 eb412ce7	no	44
Warnings:					
Information:					
5	Foreign Reference	EP1367637A1.pdf	392840 a1fcc387f6da3467baa74ad2f53057eaf af4cee2	no	29
Warnings:					
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6	Fee Worksheet (PTO-06)	fee-info.pdf	8219 14379c3a41524d26e89498od8e5cb97 68beee853	no	2
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National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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